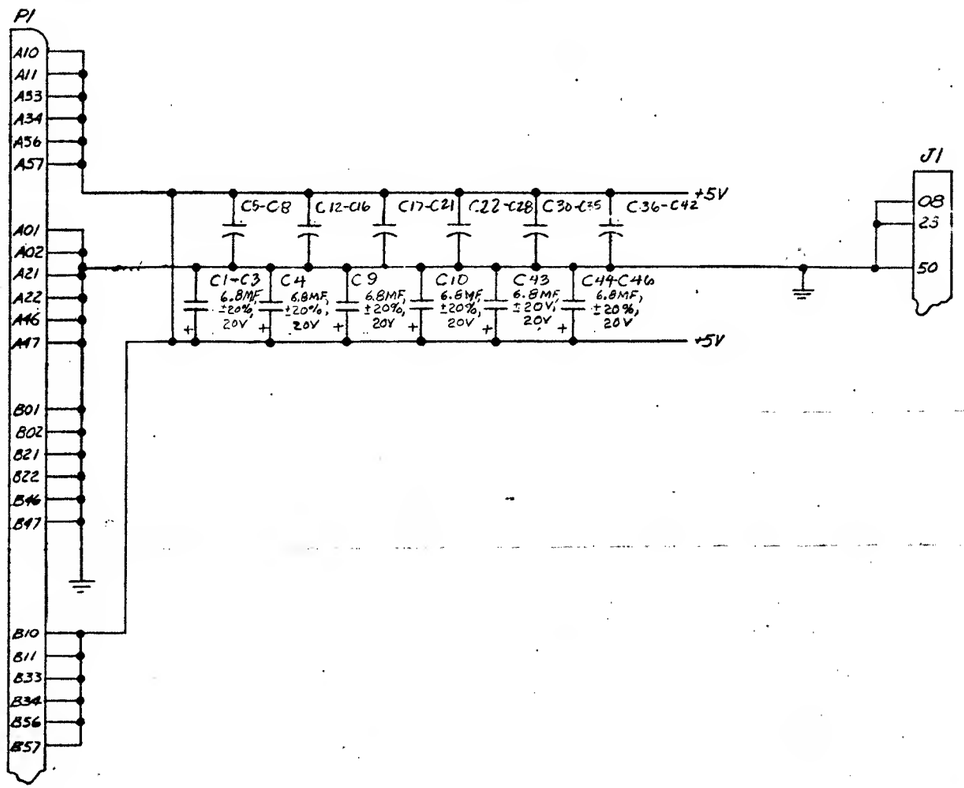
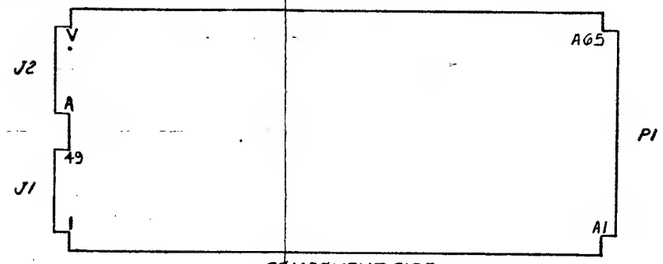
REVISIONS					
REV	DESCRIPTION	DR	CH	DATE	APPROVAL
E0	ENGINEERING RELEASE	[illegible]	RE	[illegible]	[illegible]
E1	PROD REL PER EN 8704 R	JJD	RE	7-9-81	[illegible]
E2	INCORP PER EN: 9497 H	JJD	RB	22DEC81	[illegible]





COMPONENT SIDE

TABULATION BLØCK			
DASH NO.	DESCRIPTION	BILL OF MATERIALS	JUMPER
-00	BASIC	70-53507-01	W2, W4, W6
-25	LSI 2/60 ROM	70-53507-25	W1, W3, W5

UNUSED GATES/IC			
IC DESIG	IC TYPE	UNUSED PINS IN	UNUSED PINS OUT
9	74H40	6	1,2,4,5
10	74S00	9,10,12,13	8,11
31	74H74	1,2,3	4,5,6
36	74S00	4,5	6
45	SPARE		
48	SPARE		
49	74H00	9,10,12,13	8,11
50	74S00	1,2,4,5	3,6
51	SPARE		
52	SPARE		
53	SPARE		
54	SPARE		
56	74S04	5,9,11	6,8,10

RØM CØNFIGURATIØN TABLE						
RØM	IC	-00	-25			
-01	1	47186	85107			
-02	2	47192	85113			
-03	5	47187	85108			
-04	6	47193	85114			
-05	11	47188	85109			
-06	12	47194	85115			
-07	17	47189	85110			
-08	18	47195	85116			
-09	23	47266	85111			
-10	24	47198	85119			
-11	29	47268	85118			
-12	34	47267	85112			
-13	35	47269	85120			
-14	41	47196	85117			

7. LAST REF DESIGNATION USED IC57, R2C16, RP8
6. ALL CAPACITORS ARE .022 MF +80%-20%, 20V.
5. BI DIRECTIONAL SIGNAL DENOTED BY —⋈—
4. OUTPUT SIGNAL DENOTED BY —▷.
3. INPUT SIGNAL DENOTED BY —◁.
2. RESISTORS ARE IN OHMS ±5%, 1/4W.
1. FOR DRAWING NO. SEE TABULATION BLOCK.

UNLESS OTHERWISE SPECIFIED

NOTES UNLESS SPECIFIED
1. TOLERANCES
.XX ±.03 ANGULAR
.XXX ±.010 ±½°
2. BREAK ALL SHARP EDGES .010 APPROX.
3. ALL DIM. IN INCHES

DASH NO.	NEXT ASSEMBLYS
-01	73-53507-01
-25	73-53507-25

DR.	[illegible]	15APR81
CHK.	R. ERSTAD	23APR81
DSG.	[illegible]	24APR81
ENG.		

ComputerAutomation
18651 Von Karman, Irvine, Calif 92664

TITLE
LOGIC DIAGRAM -
PROCESSOR, HALF CARD

SIZE	DWG. NO.	REV
D	75-53507-XX	E2

DO NOT SCALE DRAWING | SCALE = NONE | SHT. 1 OF 6

P1	SIGNAL	DIRECTION	LOCATION
PIN A01	GND	←	
A02	GND	↕	
A03	$\overline{IRO7}$	↕	(C22)
A04	$\overline{IRO6}$	↕	(B22)
A05	IRO0	↕	(B22)
A06	$\overline{IRI3}$	↕	(C22)
A07	$\overline{IRO8}$	←	(B22)
A08	$\overline{I815}$	→	(D34)
A09	$\overline{I67}$	→	(D34)
A10	+5V	←	
A11	+5V	←	
A12	SPARE		
A13	ALU00	→	(D28)
A14	ALU02	↕	(D28)
A15	$\overline{WMBBS}$	↕	(C37)
A16	MAD02	→	(B18)
A17	$\overline{\emptyset PDO2}$	←	(C24)
A18	$\overline{\emptyset PDO0}$	←	(A22)
A19	MAD07	→	(C18)
A20	MAD00	→	(B18)
A21	GND	←	
A22	GND	←	
A23	MAD01	→	(B18)
A24	MAD03	→	(B18)
A25	$\overline{IRI0}$	←	(C22)
A26	$\overline{MSELF}$	→	(C41)
A27	MAD05	↕	(C18)
A28	MSELF	↕	(C41)
A29	MAD06	↕	(C18)
A30	MAD12	↕	(C18)
A31	MAD10	↕	(C18)
A32	MAD14	→	(D18)
A33	+5V	←	
A34	+5V	←	
A35	MAD16	→	(D18)
A36	IØSOO	→	(C37)
A37	MAD17	→	(D18)
A38	RESERVED		
A39	PAGOO	→	(C31)
A40	PAGO1	→	(B31)
A41	SPARE		
A42	SPARE		
A43	CS02	→	(D43)
A44	SPARE		
A45	SPARE		
A46	GND	←	
A47	GND	←	
A48	BS04	←	(B48)
A49	SPARE		
A50	$\overline{CTC}$	←	(C43)
A51	SPARE		
A52		↕	
A53		↕	
A54		↕	
A55	SPARE		
A56	+5V	←	
A57	+5V	←	
A58	$\overline{NSER}$	←	(B46)
A59	INTFK	→	(B46)
A60	$\overline{INTF}$	←	(A45)
A61	$\overline{BS03}$	←	(B48)
A62	CLKEN	←	(C48)
A63	SPARE		
A64	GND	←	
A65	GND	←	

P1	SIGNAL	DIRECTION	LOCATION
PIN B01	GND	←	
B02	GND	←	
B03	2MHZ	→	(C43)
B04	IRO1	←	(C22)
B05	$\overline{IRO5}$	↕	(A22)
B06	IRI4	↕	(C24)
B07	$\overline{IRO9}$	↕	(C24)
B08	$\overline{IRI1}$	←	(B22)
B09	$\overline{IL6}$	→	(D34)
B10	+5V	←	
B11	+5V	↕	
B12	$\overline{IRI2}$	↕	(B22)
B13	$\overline{STOPI}$	←	(C42)
B14	ALU01	→	(D28)
B15	ALU03	→	(D28)
B16	RESERVED		
B17	$\overline{\emptyset PDO1}$	←	(B22)
B18	$\overline{\emptyset PDO5}$	←	(D24)
B19	CS01	→	(D45)
B20	$\overline{1MHZ}$	→	(C43)
B21	GND	←	
B22	GND	←	
B23	RESERVED		
B24	MAD04	→	(C18)
B25	$\overline{IØCLI}$	→	(B43)
B26	$\overline{\emptyset PDO2}$	←	(C22)
B27	$\overline{SACKI}$	→	(A45)
B28	$\overline{10MHZ}$	→	(C43)
B29	$\overline{\emptyset PDO3}$	←	(B24)
B30	MAD11	→	(C18)
B31	MAD13	→	(D18)
B32	MAD15	→	(D18)
B33	+5V	←	
B34	+5V	←	
B35	RESERVED		
B36	GØ	→	(C34)
B37	$\overline{IØ}$	←	(B48)
B38	RESERVED		
B39	SPARE		
B40	RESERVED		
B41	RESERVED		
B42	IØSOI	→	(C37)
B43	$\overline{PO3}$	→	(A45)
B44	SPARE		
B45	SPARE		
B46	GND	←	
B47	GND	←	
B48	PAUSF	→	(C45)
B49	RESERVED		
B50	RESERVED		
B51	RESERVED		
B52	$\overline{PO0}$	→	(B45)
B53	RESERVED		
B54	RSVP	→	(B34)
B55	IØ	→	(C37)
B56	+5V	←	
B57	+5V	←	
B58	DLY	→	(B44)
B59	INTFJ	→	(B34)
B60	XCLK	←	(C47)
B61	20.1HZ	→	(C41)
B62	$\overline{PO2}$	→	(A45)
B63	ENDP	←	(C46)
B64	GND	←	
B65	GND	←	

J1	SIGNAL	DIRECTION	LOCATION
PIN 01	$\overline{\emptyset ICLK}$	→	(B25)
02	$\overline{\emptyset IO2}$	→	(B25)
03	$\overline{FWSOO}$	→	(D25)
04	$\overline{MOISI}$	←	(D47)
05	$\overline{\emptyset IO1}$	→	(B25)
06	$\overline{IL\emptyset CK}$	←	(A20)
07	$\overline{SL\emptyset CK}$	←	(A20)
08	GND	→	
09	FWS01	→	(D25)
10	FRSOO	↕	(B28)
11	FRS01	↕	(B28)
12	ALM	↕	(C28)
13	$\overline{WSTB}$	↕	(B47)
14	$\overline{\emptyset VOO}$	↕	(B25)
15	$\overline{FWUL}$	↕	(B45)
16	LNKO2	↕	(B38)
17	$\overline{WRSOI}$	↕	(C25)
18	$\overline{FREU}$	↕	(B28)
19	$\overline{LNKOI}$	↕	(D37)
20	MRSOO	↕	(C25)
# 21	MRSOI	↓	(C25)
22	$\overline{CSOB}$	→	(D41)
23	GND	→	
24	$\overline{INHBT}$	←	(B35)
25	$\overline{RØMOO}$	→	(A37)
26	$\overline{RMAOI}$	↕	(B38)
27	$\overline{WRSOO}$	↕	(C25)
28	SIF	↕	(C28)
29	$\overline{FREL}$	↕	(B28)
30	TTMOO	↕	(D37)
31	TTMOI	↕	(D37)
32	TTMO2	↕	(D37)
33	LNKOO	↕	(D25)
34	MSEL	↕	(C28)
35	FW	↕	(D25)
36	$\overline{MWMXN}$	→	(D34)
37	TSTR	←	(A21)
38	IPCLR	→	(B41)
39	PCS00	→	(B37)
40	PCS03	→	(C37)
41	$\overline{RMXON}$	←	(A22)
42	PCSOI	→	(B37)
43	$\overline{BCLR}$	→	(C46)
44	PCS02	→	(B37)
45	DFZER	←	(D22)
46	ALCN	→	(C28)
47	$\overline{IØCLI}$	→	(A44)
48	$\overline{MRST}$	←	(B29)
49	IF	←	(B48)
50	GND	→	

J2	SIGNAL	DIRECTION	LOCATION
PIN 01	RØMO4	→	(A34)
02	RØMO6	↕	(B35)
03	$\overline{RØMO2}$	↕	(C34)
04	RØMOI	↕	(C34)
05	$\overline{RMAO2}$	→	(B37)
06	SPARE		
07	SPARE		
08	SPARE		
09	$\overline{TRØMI}$	←	(B38)
10	RØMO7	→	(B35)
11	SPARE		
12	SPARE		
13	SPARE		
14	RAEN	→	(B31)
15	SPARE		
16		↕	
17		↕	
18	SPARE		
A	$\overline{RØMO5}$	→	(B34)
B	$\overline{TRØM2}$	→	(B34)
C	$\overline{RØMO3}$	→	(C34)
D	SPARE		
E	$\overline{RMAOO}$	→	(A37)
F	SPARE		
H	SPARE		
J	SPARE		
K	$\overline{TRØM3}$	←	(B34)
L	CSO4	→	(D42)
M	SPARE		
N	SPARE		
P	SPARE		
R	$\overline{CARRY}$	→	(B42)
S	SHØRT	→	(B35)
T	STØPF	→	(C41)
U	SPARE		
V	SPARE		

RP1

	+5V	1
(C38)	$\overline{WMBBS}$	2
(D29)	ALU03	10
(D29)	ALU02	9
(D29)	ALU01	8
(D29)	ALU00	7
(D35)	$\overline{IL6}$	5
(D35)	$\overline{I67}$	4
(D35)	$\overline{I815}$	3
(B29 C24 C43, A44)	+5R2	6

RP2

1	+5V	
9	IØ	(C38)
2	RSVP	(B35)
3	INTFJ	(B35)
5	XCLK	(C47)
4	CLKEN	(C48)
10	CTC	(C42)
8	IØSOI	(C38)
7	IØSOO	(C38)
6	GØ	(C35)

RP3

	+5V	1
(B25)	ØVOO	7
(D38)	UNKOI	5
	N/C	6
	FWSOI	2
(D25)	TTMOO	3
(D38)	ØVOI	8
(B25)	FWSOO	4
(D25)	ØVO2	9
(B25)	ØVCLK	10

RP5

1	+5V	
9	FRSOI	(B29)
10	FRSOO	(B28)
3	$\overline{WRSOI}$	(C25)
7	$\overline{FREU}$	(C28)
2	N/C	
6	MRSOO	(C25)
5	MRSOI	(C25)
8	$\overline{FREL}$	(B28)
4	$\overline{WRSOO}$	(C25)

RP4

	+5V	1
(C29)	ALM	3
(C29)	SIF	5
(C28)	ALCN	4
(B38)	LNKO2	2
(D38)	TTMOI	10
(D38)	TTMO2	8
(D25)	LNKOO	9
(C28)	MSEL	7
(D25)	FW	6

RP6

1	+5V	
4	MWMXN	(D35)
9	PCS00	(B38)
10	PCS03	(C38)
8	PCSOI	(B38)
7	PCS02	(B38)
2	RØMO0	(C35)
3	RMAOI	(B38)
5	N/C	
6	N/C	

RP7

	+5V	1
(B38)	RMAO2	2
(B38)	RMAOO	3
(C35)	RØMOI	4
(C35)	RØMO2	5
(C35)	RØMO3	6
(B34)	TRØM2	8
(B34)	RØMO6	7
(B35)	RØMO5	10
(A35)	RØMO4	9

RP8

1	+5V	
9	TRØMI	(B37)
10	TRØM3	(B34)
7	RØMO7	(B35)
2	+5RI	(C41, B43, A47)
4	N/C	
6	N/C	
5	RAEN	(B43)
8	N/C	(B31)
3	SHORT	C41, C42, B44, C43 (B35)

ALL RESISTOR PACKS ARE 1K, ±5%, 1/8W

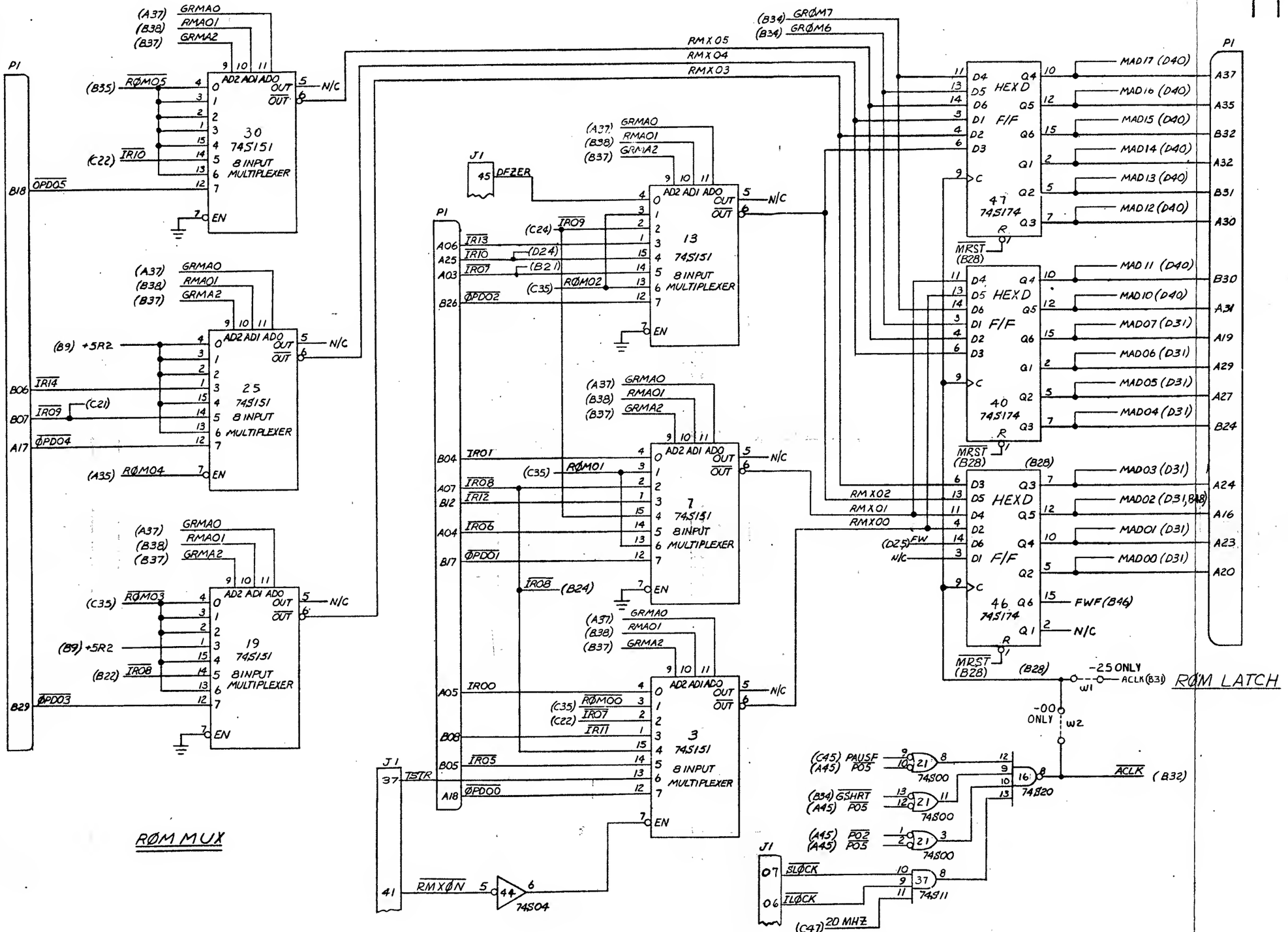
REVISIONS

REV	DESCRIPTION	DR	CH	DATE	APPROVAL

SIZE	DWG NO	REV
D	75-53507-XX	

SCALE = — SHT. 2 OF

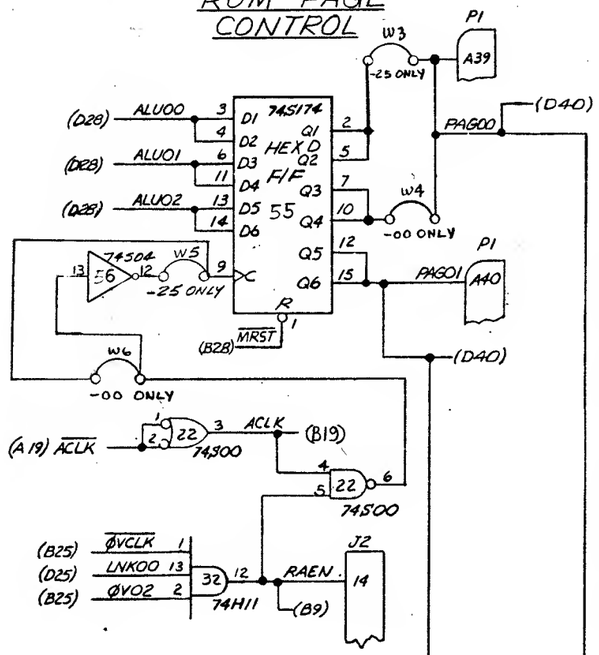
REVISIONS					
REV	DESCRIPTION	DR	CH	DATE	APPROVAL

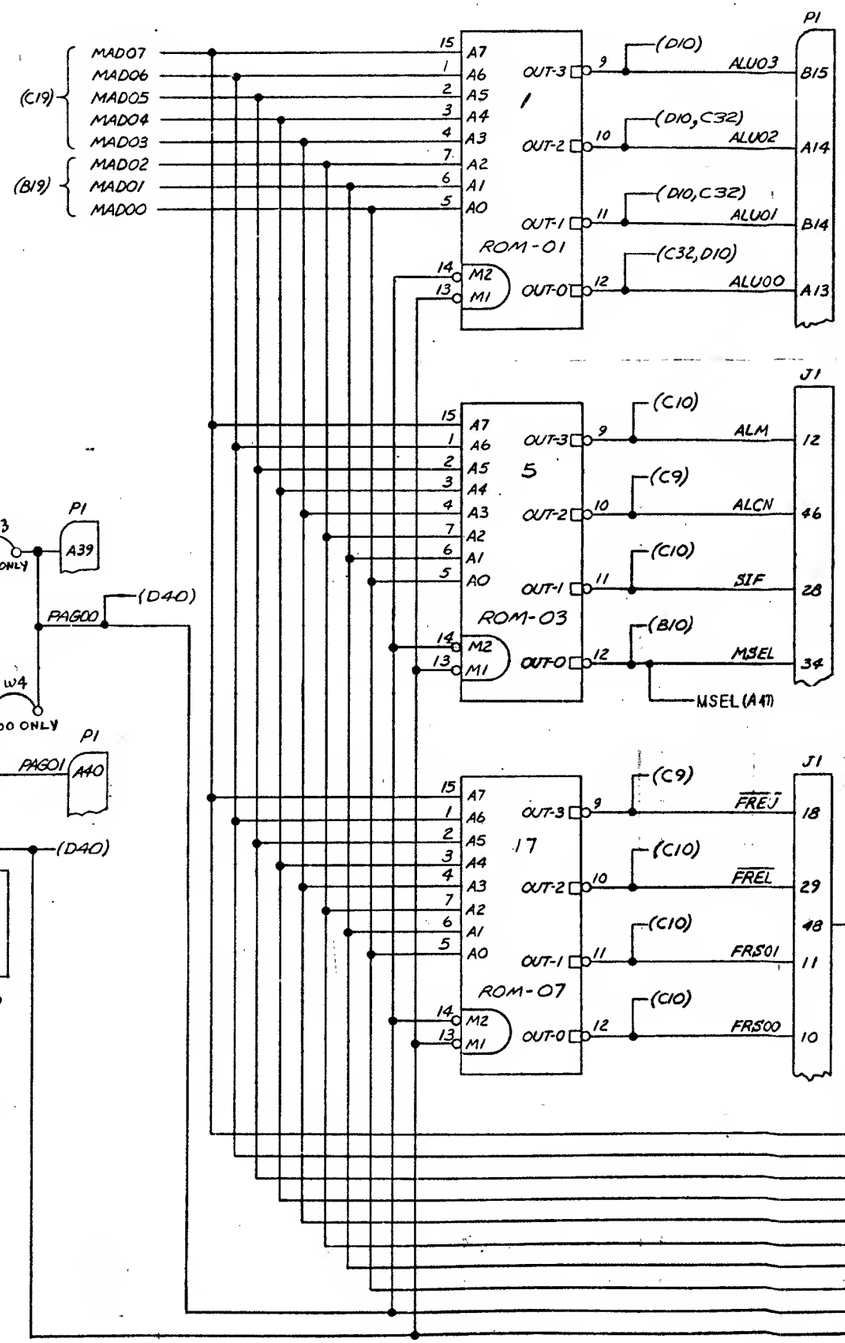


SIZE		REV
D	75-53507-XX	E2
	SCALE=	SHT. 3 OF

ROMS

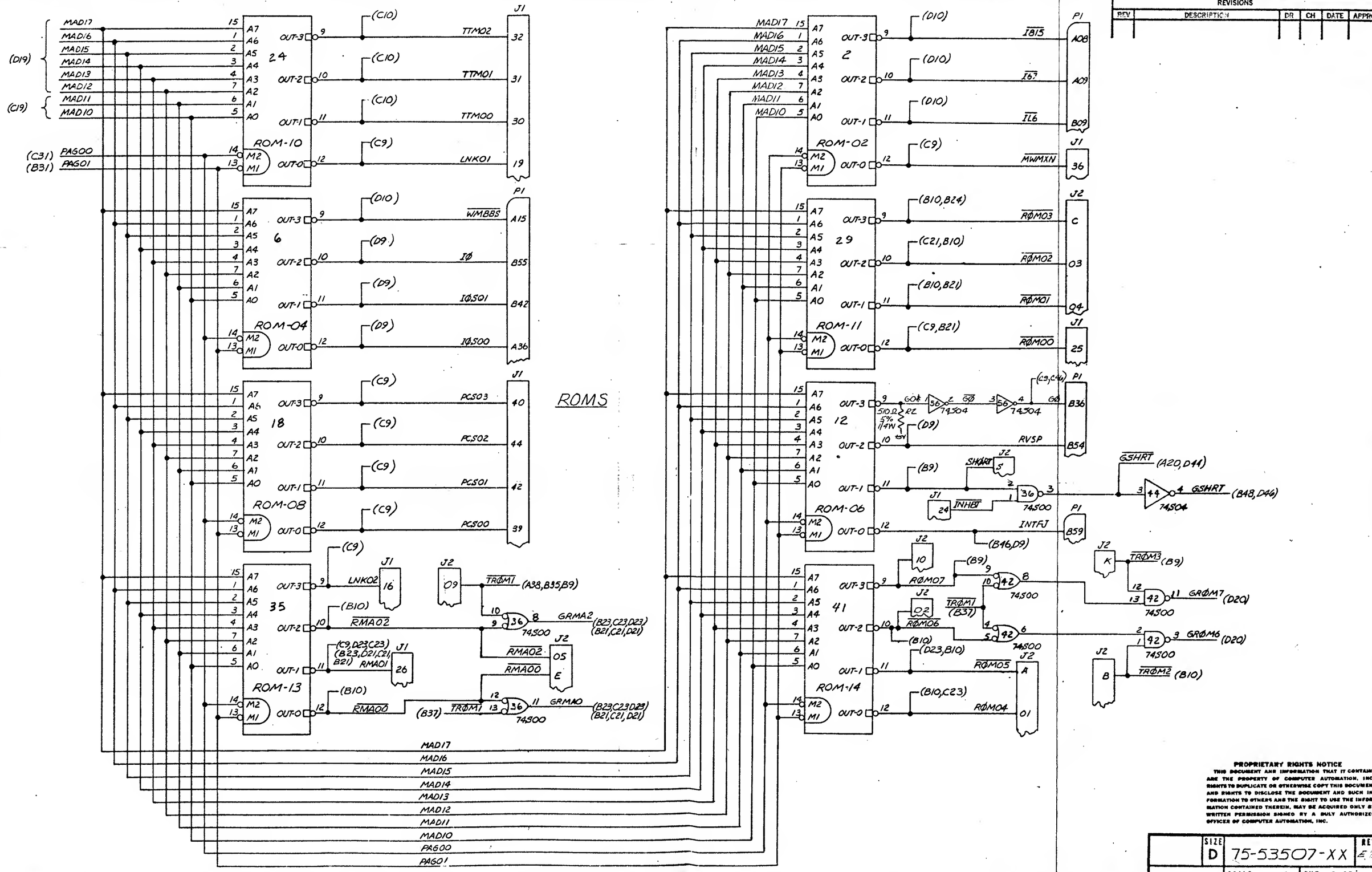
ROM PAGE CONTROL





REVISIONS					
REV	DESCRIPTION	DR	CH	DATE	APPROVAL

SIZE		REV
D	75-53507-XX	EE
SCALE=	SHT. 4 OF	


ROMS
REVISIONS
REV | DESCRIPTION | DR | CH | DATE | APPROVAL
ROM-10
ROM-04
ROM-08
ROM-13
ROM-02
ROM-11
ROM-06
ROM-14
MAD17
MAD16
MAD15
MAD14
MAD13
MAD12
MAD11
MAD10
PAG00
PAG01
TTM02
TTM01
TTM00
LNK01
WMBBS
IØ
IØS01
IØS00
PCS03
PCS02
PCS01
PCS00
LNK02
RMA02
RMA01
RMA00
GRMA2
GRMAO
RØM03
RØM02
RØM01
RØM00
RØM07
RØM06
RØM05
RØM04
MWMXN
RVSP
INTFJ
GSHRT
GRØM7
GRØM6
TRØM3
TRØM2
TRØM1
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SIZE D
75-53507-XX
REV E2
SCALE=
SHT. 5 OF

REVISIONS					
REV	DESCRIPTION	DR	CH	DATE	APPROVAL

PHASE CLØCK

WATCH DØG TIMER AND DELAY

BUS CLØCKS

TIMING DECØDE

SIZE		REV
D	75-53507-XX	E
SCALE=	SHT. 6 OF 6	

ComputerAutomation® 18651 Von Karman, Irvine, Calif.

ENGINEERING NOTICE

NO. 19893H

DOCUMENT NO.	REV. iS	REV. WAS	TITLE	INCORP. DATE
75-53507-XX	E4	E3	LOGIC,LSI PROCESSOR HALF CARD.	
74-53507-00	E4	E3	DETAIL	
73-53507-XX	E4	E3	ASSY	
70-53507-00	E4	E3	B/M	6 JUN 83
70-53507-25	E4	E3	B/M	7 JUN 83

EFFECTIVITY NOTES:

REASON FOR CHANGE:

JUMPER SPECIFICATIONS ARE INCORRECT.

REA NO. 14102

CO-ORD WITH:

DESCRIPTION OF CHANGE:

A REWORK
1. REMOVE JUMPERS
   75-53507-00 W4
   75-53507-25 W3
2. ADD JUMPERS
   75-53507-00 W3
   75-53507-25 W4

(NOTE: JUMPERS 1,2,5 AND 6 NOT AFFECTED.)

TYPE	
AEN	☐
STOP ORDER	☐
DEVIATION	☐
RELEASE	☐
STANDARD	■

CLASS	
A-MAND/FUNC	■
B-NON-MAND/FUNC	☐
C-RECORD CHG	☐

AFFECTED ITEMS	PRIM.	SEC.
HARDWARE CHG.	■	☐
SOFTWARE CHG.	☐	☐
PUBL. CHG.	☐	☐
CAPABLE CHG.	☐	☐
DOC. CHG.	■	☐
CONFIGURATIONS	☐	☐
PROCEDURES	☐	☐
TOOLING	☐	☐
TEST EQUIP.	☐	☐

EFFECTIVITY	U	R	S	P
ACTIVITY				
NOTIFY VEND		X		
IN STOCK		X		
KITTING		X		
ASSEMBLY		X		
TOUCH UP		X		
IPT		X		
FIN GOODS		X		
CUST. RET.		X		

REWK TEST REQ'D	
CONTINUITY	☐
CABLE SCAN	☐
CAPABLE	☐
MEMORY	☐
CARD	☐
FINAL	☐
NO TEST REQ'D	■

APPROVALS	
ENGR. B Gross	—
SOFTWARE	—
Q.A. M Hamrick	
CAP. TEST	
MAST SCHED	
MATERIALS	
TEST ENGR.	
TECH SERV	
CUST SERV	
MFG. ENGR	—
PUBLICATIONS	—

DR. BY: D. PARKER 15JUN83
CHKD. BY:
REL. BY:
DATE:

97NC SHEET 1 OF 3

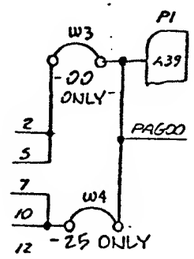
A. 70-53507-00,25 B/M'S
1. CHANGE REF DESIG OF ITEM 26

IS: -00 W2,W3,W6
-25 W1,W4,W5

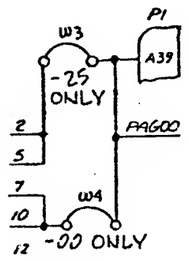
WAS: -00 W2,W4,W6
-25 W1,W3,W5

B. 75-53507-XX LOGICS
1. ADD JUMPERS W4,W3

IS:



WAS:



ComputerAutomation®

SHEET 2 OF

C. 75-53507-XX

1. CHANGE TABULATION BLOCK JUMPERS

IS:

COMPONENT SIDE

TABULATION BLOCK ·			
DASH NO.	DESCRIPTION	BILL OF MATERIALS	JUMPER
-00	BASIC	70-53507-01	W2,W3,W6
-25	LSI2/60 ROM	70-53507-25	W1,W4,W5

WAS:

COMPONENT SIDE

TABULATION BLOCK ·			
DASH NO.	DESCRIPTION	BILL OF MATERIALS	JUMPER
-00	BASIC	70-53507-01	W2,W4,W6
-25	LSI2/60 ROM	70-53507-25	W1,W3,W5